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For: LIQUID CRYSTAL DISPLAY

Enclosed are:

- (X) 29 pages of specification, including 16 claims and an abstract.
(X) an executed oath or declaration, with power of attorney.
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() sheet(s) of informal drawing(s).
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Fee Calculation For Claims As Filed

a) Basic Fee					\$ 690.00
b) Independent Claims	<u>1</u>	-	3	=	<u> </u> x \$ 78.00 = \$ <u> </u>
c) Total Claims	<u>16</u>	-	20	=	<u> </u> x \$ 18.00 = \$ <u> </u>
d) Fee for Multiple Claims					\$260.00 = \$ <u> </u>
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TITLE OF THE INVENTION
LIQUID CRYSTAL DISPLAY

BACKGROUND OF THE INVENTION

5 The present invention relates to a liquid crystal display, and more particularly to a liquid crystal display using a liquid crystal having a spontaneous polarization.

Along with the recent developments of the so-called office automation (OA), OA apparatuses, typically exemplified by
10 word-processors and personal computers, have been widely used. As such OA apparatuses have become prevalent in offices, there have been ever-increasing demands for portable-type OA apparatuses that can be used in offices as well as outdoors, and there have been also demands for small-size and
15 light-weight of such apparatuses. Here, liquid crystal displays have come to be widely used as one of the means to achieve such an objective. Liquid crystal displays not only achieve small-size and light-weight, but also include an indispensable technique in an attempt to achieve low power
20 consumption in portable OA apparatuses that are driven by batteries.

The liquid crystal displays are mainly classified into the reflection-type and the transmission-type. In the reflection-type liquid crystal displays, light rays that have
25 been made incident on the front face of a liquid crystal panel

are reflected by the back face of the liquid crystal panel so that an image is visualized by the reflected light. In the transmission-type liquid crystal displays, transmitted light from a light source (back-light) placed behind the back face of a liquid crystal panel is used to visualize an image. Although those of the reflection-type are inferior in visibility due to irregularity in the amount of reflected light that depends on environment conditions, they are inexpensive and widely used as display devices with mono-color (for example, black/white display, etc.) for such as calculators and watches. However, they are not suitable for display devices for personal computers, etc. which carry out a multi-color or full-color display. For this reason, in general, transmission-type liquid crystal displays are used as display devices for personal computers, etc. which carry out a multi-color or full-color display.

Here, currently-used color liquid crystal displays are generally classified into the STN (Super Twisted Nematic) type and the TFT-TN (Thin Film Transistor-Twisted Nematic) type based upon the liquid crystal type to be used. Although those of the STN type have comparatively low manufacturing costs, they are susceptible to cross-talk, and comparatively slow in response speeds; therefore, they are not suitable for display for animation pictures. In contrast, those of the TFT-TN type have higher display quality as compared with the STN type;

however, since, at present, their liquid crystal panel has a light transmittance as low as 4%, a back-light with high luminance is required. For this reason, those of the TFT-TN type have greater power consumption due to the back-light, resulting in a problem in use of carrying battery power-source. Moreover, the TFT-TN type have other problems with the response speed, particularly slow in response speed for displaying half tones, narrow viewing angle, difficulty in adjusting the color balance, etc.

In order to solve the above-mentioned problems, a new type of liquid crystal devices have been developed in which a liquid crystal (ferroelectric liquid crystal or anti-ferroelectric liquid crystal, etc.) having a spontaneous polarization, which has a high response speed of hundreds to several μ seconds order to an applied electric field, is used and this liquid crystal is driven by using a switching element such as a TFT. In the case when such a ferroelectric liquid crystal or an anti-ferroelectric liquid crystal is used, the liquid crystal molecules are constantly maintained in parallel with the substrate (glass substrate) independent of the presence or absence of an applied voltage so that it is possible to achieve a very wide viewing angle.

However, in the liquid crystal display using a liquid crystal having a spontaneous polarization, such as a ferroelectric liquid crystal or an anti-ferroelectric liquid

crystal, that is driven by using a switching element, there is a problem in which the driving operation requires a greater voltage.

FIG. 1 is a graph that shows the relationship between the applied voltage and the light transmittance in a conventional liquid crystal display using a ferroelectric liquid crystal. FIG. 1 shows that an applied voltage of ± 15 V is required to adjust the light transmittance of the liquid crystal; however, from the viewpoint of voltage resistance, etc., of the liquid crystal driving driver IC, etc., it is preferable to set the applied voltage to not more than ± 10 V.

BRIEF SUMMARY OF THE INVENTION

The present invention has been devised to solve the above-mentioned problems, and its objective is to provide a liquid crystal display which adjusts the magnitude of a spontaneous polarization and the relative dielectric constant of a liquid crystal to appropriate values so that it can drive the liquid crystal even by using a low applied voltage.

The inventors, etc. of the present invention have carried out various experiments as described below so as to develop a liquid crystal display which can drive a liquid crystal by using even a low applied voltage.

First, as shown in Table 1, a plurality of liquid crystals whose spontaneous polarization had a magnitude in the range of

2.5 to 27 nC/cm² and whose relative dielectric constant was set in the range of 3 to 8 were prepared. Here, the magnitude of the spontaneous polarization refers to a 1/2 of a quantity of charge that flows when the spontaneous polarization is inverted.

After glass substrates having transparent electrodes with an electrode area of 1.77 cm² had been washed, polyimide was applied to these by a printing method, and these were baked for one hour at 200°C to form polyimide films as alignment films each having a thickness of approximately 200 Å.

Moreover, these alignment films were rubbed with a cloth made of rayon, and lapped with a gap between them maintained by spacers made of silica having an average particle size of 1.6 μm; thus, an empty panel was formed. A ferroelectric liquid crystal as shown in Table 1 was sealed between the alignment films of this empty panel to form a liquid crystal display element.

Then, to the liquid crystal display element thus formed is connected an FET (Field-Effect transistor) serving as a switching element, and a voltage is applied to the liquid crystal display element through this FET switching element; thus, the driving voltage and the holding rate were measured. FIG. 2 is an explanatory drawing that explains the definition of the driving voltage and that indicates the relationship between the light transmittance and the applied voltage of the

liquid crystal. As shown in FIG. 2, the driving voltage represents an applied voltage that is required to allow the light transmittance of the liquid crystal to reach 100%.

Moreover, the holding rate is a value obtained as follows:

5 FIG. 3 is an explanatory drawing that explains the definition of the holding rate, FIG. 3A shows changes in the applied voltage at the times of ON/OFF of the FET, and FIG. 3B shows changes in the voltage inside the liquid crystal display element at the times of ON/OFF of the FET.

10 In order to obtain the holding rate, the voltage inside the liquid crystal display element is measured during a period from the turning on of the FET to the next turning on of the FET. As shown in FIG. 3B, since a voltage is being applied during the time in which the FET is turned on, the voltage
15 inside the liquid crystal display is maintained in a high level; however, when the FET is turned off, the voltage drops in the liquid crystal display element.

 In FIG. 3B, an area a represents an integral value of the voltage inside the liquid crystal display element during a
20 period from the turning on of the FET to the next turning on of the FET. Moreover, an area a + b represents an integral value of the voltage inside the liquid crystal display element in the case when the applied voltage has been maintained during a period from the turning on of the FET to the next turning on of
25 the FET. Thus, the area ratio of the area a to the area a + b

is defined as the holding rate.

Here, in this experiments, the time in which the FET is maintained ON is $5\mu s$, and the cycle thereof is set to be 2.8 ms.

FIG. 4 is a graph that shows the relationship between the holding rate and $(Q - 2Ps)/Q$ per unit area at the time of applying a driving voltage. Here, Q represents a quantity of charge that is injected to the liquid crystal display element at the time of applying the driving voltage. As shown in FIG. 4 there is a strong correlation between the holding rate and $(Q - 2Ps)/Q$ at the time of applying a driving voltage, and these values are virtually coincident with each other. It is considered that this phenomenon is due to the influence of an inversion of a spontaneous polarization.

In other words, the quantity of charge, which has been injected to the liquid crystal display element while the FET is maintained ON, is consumed by the inversion of the spontaneous polarization to a quantity corresponding to $2Ps$ while the FET is maintained OFF; therefore, it is considered that the holding rate and $(Q - 2Ps)/Q$ are virtually coincident with each other.

Based upon the above-mentioned phenomenon, supposing the holding rate = $(Q - 2Ps)/Q$, $Ps \leq Q/2$ is obtained since the holding rate is not less than 0. Thus, the inventors, etc. of the present invention have found that, when the magnitude of the spontaneous polarization of a liquid crystal is not more

than 1/2 of the maximum quantity of charge that is to be injected to a liquid crystal display element (hereinafter, referred to as a pixel) corresponding to each pixel while the switching element is maintained ON, it is possible to drive the liquid crystal display by utilizing the quantity of charge accumulated in the pixel. Based upon this finding, they have invented a liquid crystal display described below.

A liquid crystal display in accordance with the present invention, which has a liquid crystal having a spontaneous polarization, held between a pair of opposing substrates, with an electrode and a switching element corresponding to a pixel being placed on an inner surface of one of the substrates, is characterized in that the magnitude of the spontaneous polarization of the liquid crystal is not more than 1/2 of a maximum quantity of charge that is injected to each pixel when the switching element is turned on.

In this case, a liquid crystal having a spontaneous polarization, such as a ferroelectric liquid crystal and an anti-ferroelectric liquid crystal, is sealed between the pair of opposing substrates. Further, the magnitude of the spontaneous polarization of the liquid crystal sealed therein is more than 1/2 of a maximum quantity of charge that is to be injected to each pixel when the switching element is in an on-state. Therefore, as compared with conventional liquid crystal displays, the present liquid crystal display makes it

possible to drive the liquid crystal with a lower applied voltage.

Moreover, a liquid crystal display in accordance with the present invention is characterized in that the magnitude of the spontaneous polarization of the liquid crystal is not more than 15 nC/cm².

Furthermore, another liquid crystal display in accordance with the present invention is characterized in that the magnitude of the spontaneous polarization of the liquid crystal is not more than 10 nC/cm².

Still another liquid crystal display in accordance with the present invention is characterized in that the magnitude of the spontaneous polarization of the liquid crystal is not more than 7 nC/cm².

Moreover, still another liquid crystal display in accordance with the present invention is characterized in that the relative dielectric constant of the liquid crystal is not less than 3.

In liquid crystal displays, in general, from the viewpoint of voltage resistance of a liquid crystal driver IC, etc., it is required that the applied voltage is not more than ± 10 V. Moreover, at present, the output voltages of generally-used liquid crystal driver ICs are ± 10 V, ± 7 and ± 5 V.

Supposing the applied voltage is ± 10 V, and that charge of approximately 3 nC is injected to the pixel electrodes per a

square centimeter, the quantity of charge to be injected to each pixel is approximately 30 nC/cm^2 . Here, as described earlier, the magnitude of the spontaneous polarization is $1/2$ the quantity of charge that flows at the time of inversion of the spontaneous polarization. Therefore, in the case when the magnitude of the spontaneous polarization of the liquid crystal is not more than 15 nC/cm^2 , that is $1/2$ of the value 30 nC/cm^2 , it becomes possible to drive the liquid crystal by using a liquid crystal driver IC having an output voltage of $\pm 10 \text{ V}$.

Moreover, supposing the applied voltage is ± 7 , the quantity of charge to be injected to each pixel is approximately 20 nC/cm^2 . Therefore, in the same manner as described above, in the case when the magnitude of the spontaneous polarization of the liquid crystal is not more than 10 nC/cm^2 , that is $1/2$ of the value 20 nC/cm^2 , it becomes possible to drive the liquid crystal by using a liquid crystal driver IC having an output voltage of $\pm 7 \text{ V}$.

Furthermore, supposing the applied voltage is ± 5 , the quantity of charge to be injected to each pixel is approximately 15 nC/cm^2 . Therefore, in the same manner as described above, in the case when the magnitude of the spontaneous polarization of the liquid crystal is not more than 7 nC/cm^2 , that is $1/2$ of the value 15 nC/cm^2 , it becomes possible to drive the liquid crystal by using a liquid crystal driver IC having an output voltage of $\pm 5 \text{ V}$.

As described above, the quantity of charge injected to each pixel is mainly determined by a driving voltage; however, the quantity of charge varies depending on the relative dielectric constant of the liquid crystal. When the relative dielectric constant is not less than 3, the quantity of charge as described above can be injected to each pixel.

Here, the liquid crystal can be driven as long as the inversion in the spontaneous polarization occurs; therefore, the spontaneous polarization may be any magnitude greater than 0 nC/cm².

Moreover, a liquid crystal display in accordance with the present invention is further provided with a back-light for emitting white light and color filters of three primary colors placed between the above-mentioned substrates, and characterized in that the emitted light is selectively transmitted through the color filters of the three primary colors so as to carry out a color display.

In this case, the back-light is allowed to emit white light, and this light is selectively transmitted through the color filters of red, green and blue so as to carry out a color display.

Moreover, another liquid crystal display of the present invention is further provided with a back-light having light sources for respectively emitting light rays of the three primary colors, and characterized in that these light sources

are allowed to emit light rays in a time divided manner in synchronism with ON/OFF driving processes of the switching element so that it is possible to carry out a color display.

In this case, the light sources for respectively emitting
5 light rays of the three primary colors are allowed to emit light rays in a time divided manner in synchronism with ON/OFF driving processes of the switching element so that it becomes possible to carry out a color display without using color filters.

10 The above and further objects and features of the invention will more fully be apparent from the following detailed description with accompanying drawings.

BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE DRAWING

15 FIG. 1 is a graph that shows the relationship between the applied voltage and the light transmittance in a conventional liquid crystal device;

FIG. 2 is an explanatory drawing that shows the definition of a driving voltage;

20 FIGS. 3A and 3B are explanatory drawings that show the definition of a holding rate;

FIG. 4 is a graph that shows the relationship between the holding rate and the calculated value;

25 FIG. 5 is a schematic perspective view that shows an example of the entire structure of a liquid crystal display in

accordance with the present invention;

FIG. 6 is a schematic cross-sectional view that shows a liquid crystal panel and a back-light used in the liquid crystal display of the present invention;

5 FIG. 7 is a graph that shows the relationship between the applied voltage and the light transmittance in Example 1;

FIG. 8 is a graph that shows the relationship between the applied voltage and the light transmittance in Example 2;

10 FIG. 9 is a graph that shows the relationship between the applied voltage and the light transmittance in Example 3;

FIG. 10 is a graph that shows the relationship between the applied voltage and the light transmittance in Example 4; and

FIG. 11 is a schematic drawing that shows a structural example of a light source (LED array) of Embodiment 2.

15

DETAILED DESCRIPTION OF THE INVENTION

Referring to Figures, the following description will discuss one embodiment of the present invention in detail.

20 FIG. 5 is a schematic perspective view that shows an example of the entire structure of a liquid crystal display in accordance with the present invention. As illustrated in FIG. 5, a liquid crystal panel in the liquid crystal display of the present invention is provided as a structure located between two polarizing films 1 and 5. More specifically, this
25 structure includes a polarizing film 1, a glass substrate 2, a

common electrode 3, a glass substrate 4 and a polarizing film 5 that are stacked in this order from the top-layer (surface) side to the bottom-layer (rear face) side.

Moreover, a back-light, constituted by a light source 7 and a light-guiding and diffusing plate 6, is arranged on the bottom-layer side of the liquid crystal panel.

Pixel electrodes 10, arranged in a matrix form, are formed on the surface of the glass substrate 4 on the common electrode 3 side. A liquid crystal driving control means 20 constituted by a data driver, a scan driver, etc. is connected between the common electrode 3 and the pixel electrodes 10. Here, each pixel electrode 10 is on/off controlled by a TFT 11, and each TFT 11 is driven by a data driver and a scan driver that respectively turn on/off data lines 12 and scanning lines 13 selectively. Then, the intensity of transmitted light of each pixel is controlled in accordance with data from the data line 12.

FIG. 6 is a schematic cross-sectional view that shows the liquid crystal panel and the back-light.

An alignment film 15 is formed on the upper face of the pixel electrodes 10 on the glass substrate 4, and an alignment film 14 is formed on the lower face of the common electrode 3; thus, a ferroelectric liquid crystal is filled into the void between the alignment films 14 and 15 so that a liquid crystal layer 16 is formed. Here, reference number 17 represents

spacers that maintain the layer thickness of the liquid crystal layer 16.

(Embodiment 1)

5 In the liquid crystal display of Embodiment 1 of the present invention, a back-light of white light is used, and color filters of three primary colors (placed on the glass substrate 2 on the surface side correspondingly to each the pixel electrodes 10) are used so that the white light is
10 selectively transmitted so as to carry out a color display.

 First, the liquid crystal panel, shown in FIG. 5, was manufactured as follows: Individual pixel electrodes 10 were arranged with pitches $0.1025\text{ mm} \times 0.3075\text{ mm}$ so as to form a matrix having pixels of $800 \times 3\text{ (RGB)} \times 600$ with a diagonal
15 length of 12.1 inches; thus, a TFT substrate was formed. After this TFT substrate and a glass substrate 2 having the color filters and a common electrode 3 had been washed, polyimide was coated to these by using a spin coater, and baked for one hour at 200°C to form alignment films 14 and 15 made of
20 polyimide films each having a thickness of approximately 200 \AA .

 Moreover, these alignment films 14 and 15 were rubbed with a cloth made of rayon, and lapped with a gap being maintained between them by spacers 17 made of silica having an average particle size of $1.6\text{ }\mu\text{m}$; thus, an empty panel was formed. A
25 ferroelectric liquid crystal with naphthalene-series liquid

crystal being the main component was sealed between the alignment films 14 and 15 of this empty panel. This naphthalene-series liquid crystal has a spontaneous polarization. The panel thus formed was sandwiched by two polarizing films 1 and 5 (made by Nitto Denko: NPF-EG1225DU) maintained in a cross-nicol state so that a dark state could be formed when the ferroelectric liquid crystal molecules tilted to one side; thus, a liquid crystal panel was formed.

In order to reduce the driving voltage to a low level in the liquid crystal display of the present invention provided with the liquid crystal panel manufactured as described above, it is necessary to adjust the spontaneous polarization of the liquid crystal to an appropriate magnitude. The following description will discuss some examples that can reduce the driving voltage to a low level.

(Example 1)

In Example 1, the ferroelectric liquid crystal sealed in the liquid crystal panel has relative dielectric constant of approximately 6 and a magnitude of the spontaneous polarization of 13.5 nC/cm^2 .

Then, a voltage was applied to the liquid crystal through the switching element of the TFT 11 so that the applied voltage and the light transmittance were measured. Consequently, as shown in FIG. 7, the light transmittance varied in proportion

to the applied voltage and came to be saturated at approximately 10 V. Therefore, the liquid crystal display of the present example can be driven by using a liquid crystal driver IC of an output voltage of ± 10 V.

5 Here, in the case when a voltage of 10 V was applied during the ON-state of the switching element, the quantity of charge injected to each pixel was 29 nC/cm².

Next, an explanation will be given of the displaying operation of the liquid crystal display of the present
10 invention in accordance with Example 1. In the liquid crystal display, writing scanning processes are carried out twice in each of frames. Here, in the first writing scanning (data-writing scanning), a signal having a voltage corresponding to pixel data is supplied to each pixel of the
15 liquid crystal panel from a data driver of a liquid crystal driving control means 20. Thus, the light transmittance was adjusted by the applied voltage so that an image corresponding to the pixel data is displayed.

Then, in the second writing scanning (data-erasing
20 scanning), a signal having a voltage with a reverse polarity to that of the first writing scanning is supplied to each pixel of the liquid crystal panel from the data driver of the liquid crystal driving control means 20. Thus, the voltage with the same intensity as that of the first writing scanning with the
25 reverse polarity is applied to each pixel of the liquid crystal

panel so that the display of each pixel of the liquid crystal panel is erased.

Between the first writing scanning (data-writing scanning) and the second writing scanning (data-erasing scanning),
5 voltages of signals supplied to each pixel in the liquid crystal panel have the same magnitude with only different polarities, with the result that it is possible to prevent a DC component from being applied to the liquid crystal.

10 (Example 2)

In Example 2, the ferroelectric liquid crystal sealed in the liquid crystal panel has a relative dielectric constant of approximately 6 and a magnitude of the spontaneous polarization of 8.2 nC/cm².

15 Then, a voltage was applied to the liquid crystal through the switching element of the TFT 11 so that the applied voltage and the light transmittance were measured. Consequently, as shown in FIG. 8, the light transmittance varied in proportion to the applied voltage and came to be saturated at
20 approximately 6.5 V. Therefore, the liquid crystal display of the present example can be driven by using a liquid crystal driver IC of an output voltage of ± 7 V.

Here, in the case when a voltage of 6.5 V was applied during the ON-state of the switching element, the quantity of
25 charge injected to each pixel was 19.7 nC/cm².

(Example 3)

In Example 3, the ferroelectric liquid crystal sealed in the liquid crystal panel has a relative dielectric constant of approximately 5 and a magnitude of the spontaneous polarization of 6.3 nC/cm².

Then, a voltage was applied to the liquid crystal through the switching element of the TFT 11 so that the applied voltage and the light transmittance were measured. Consequently, as shown in FIG. 9, the light transmittance varied in proportion to the applied voltage and came to be saturated at approximately 5 V. Therefore, the liquid crystal display of the present example can be driven by using a liquid crystal driver IC of an output voltage of ± 5 V.

Here, in the case when a voltage of 5 V was applied during the ON-state of the switching element, the quantity of charge injected to each pixel was 14 nC/cm².

(Example 4)

In Example 4, the ferroelectric liquid crystal sealed in the liquid crystal panel has a relative dielectric constant of approximately 3 and a magnitude of the spontaneous polarization of 5.1 nC/cm².

Then, a voltage was applied to the liquid crystal through the switching element of the TFT 11 so that the applied voltage and the light transmittance were measured. Consequently, as

shown in FIG. 10, the light transmittance varied in proportion to the applied voltage and came to be saturated at approximately 7 V. Therefore, the liquid crystal display of the present example can be driven by using a liquid crystal driver IC of an output voltage of ± 7 V.

Here, in the case when a voltage of 7 V was applied during the ON-state of the switching element, the quantity of charge injected to each pixel was 10.5 nC/cm^2 .

With respect to the displaying operation of the liquid crystal displays in accordance with Examples 2, 3 and 4 of the present invention, since it is carried out in the same manner as Example 1, the description thereof is omitted.

(Embodiment 2)

In the liquid crystal display of Embodiment 2, different from Embodiment 1, no color filters were used, and a back-light was allowed to emit three-color light rays in a time divided manner so as to carry out a full-color display.

As illustrated in a schematic drawing in FIG. 11, a light source 7 in Embodiment 2 is provided as an LED array in which LEDs for emitting three primary colors, that is, red (R), green (G) and blue (B), are arranged in this order in succession on the face opposing to the light-guiding and diffusing plate 6. The light-guiding and diffusing plate 6 guides light rays emitted from the respective LEDs of such a light source 7 to

its entire surface and diffuses them toward the upper face;
thus, it functions as a light-emitting area.

Then, the LEDs of the back-light are allowed to emit light
rays in the order of red, green and blue by specified periods,
5 and in synchronism with this operation the respective pixels in
the liquid crystal panel are switched by lines so as to carry
out a display. During the emissions of the respective colors,
writing scanning operations (first data-writing scanning and
second data-erasing scanning) are carried out twice in the same
10 manner as Embodiment 1 so that an image corresponding to pixel
data is displayed. Here, a timing is adjusted so that the end
timing of the second scanning (data-erasing scanning) for a
certain color is coincident with the start timing of the first
scanning (data-writing scanning) of the next color.

15 The liquid crystal panel of the present Embodiment was
manufactured as follows: Individual pixel electrodes 10 were
arranged with pitches $0.24\text{ mm} \times 0.24\text{ mm}$ so as to form a matrix
format having pixels of 1024×768 with a diagonal length of 12.
1 inches; thus, a TFT substrate was formed. After this TFT
20 substrate and a glass substrate 2 having a common electrode 3
had been washed, polyimide was coated to these by using a spin
coater, and baked for one hour at 200°C to form alignment films
made of polyimide films each having a thickness of
approximately 200\AA .

25 Moreover, these alignment films were rubbed with a cloth

made of rayon, and lapped with a gap being maintained between them by spacers made of silica having an average particle size of $1.6\mu\text{m}$; thus, an empty panel was formed. A ferroelectric liquid crystal with naphthalene-series liquid crystal being the main component was sealed between the alignment films and of this empty panel. This naphthalene-series liquid crystal has a spontaneous polarization. The panel thus formed was sandwiched by two polarizing films 1 and 5 maintained in a cross-nicol state so that a dark state could be formed when the ferroelectric liquid crystal molecules tilted to one side; thus, a liquid crystal panel was formed.

In the case of Embodiment 2 also, in the same manner as the respective Examples of Embodiment 1, when a liquid crystal with the magnitude of its spontaneous polarization and relative dielectric constant being adjusted is applied, it becomes possible to drive the liquid crystal by using a low voltage.

(Comparative Example)

In this Comparative Example, the ferroelectric liquid crystal sealed in the liquid crystal panel was set to have a magnitude of the spontaneous polarization of 21.8 nC/cm^2 with a rough relative dielectric constant.

Then, a voltage was applied to the liquid crystal through the switching element of the TFT 11 so that the applied voltage and the light transmittance were measured. Consequently, the

light transmittance varied in proportion to the applied voltage and came to be saturated at approximately 15 V. The results of the measurements are given in the graph showing the relationship between the applied voltage and the light transmittance in the conventional liquid crystal display shown in FIG. 1. In this manner, the liquid crystal display of Comparative Example fails to drive the liquid crystal even upon application of a driver IC of an output voltage of ± 10 V.

Here, in the case when a voltage of 15 V was applied during the ON-state of the switching element, the quantity of charge injected to each pixel was 46 nC/cm².

Additionally, in the above-mentioned Examples, a ferroelectric liquid crystal was used; however, any liquid crystal may be used as long as it has a spontaneous polarization; and liquid crystals such as an anti-ferroelectric liquid crystal and a DHF (deformed helical ferroelectric liquid crystal), may be used with the same effects.

As this invention may be embodied in several forms without departing from the spirit of essential characteristics thereof, the present embodiment is therefore illustrative and not restrictive, since the scope of the invention is defined by the appended claims rather than by the description preceding them, and all changes that fall within metes and bounds of the claims, or equivalence of such metes and bounds thereof are therefore intended to be embraced by the claims.

[Table 1]

No.	SPONTANEOUS POLARIZATION (Ps) nC/cm ²	RELATIVE DIELECTRIC CONSTANT	No.	SPONTANEOUS POLARIZATION (Ps) nC/cm ²	RELATIVE DIELECTRIC CONSTANT
1	12.82	6.17	8	7.84	6.08
2	14.25	6.64	9	4.13	5.86
3	7.99	5.67	10	7.00	5.54
4	2.52	3.18	11	5.30	4.17
5	3.85	5.59	12	5.40	6.29
6	27.53	5.89	13	6.64	6.93
7	7.43	3.01	14	7.27	8.21

CLAIMS

1. A liquid crystal display, comprising:

liquid crystal display elements having a pair of substrates with a liquid crystal therebetween having a spontaneous polarization; and

an electrode corresponding to a pixel and a switching element that are placed on an inner surface of one of the substrates, the switching element being allowed to drive the liquid crystal corresponding to a pixel when turned on,

wherein the spontaneous polarization of the liquid crystal is a magnitude of not more than $1/2$ of a maximum quantity of charge that is injected to the liquid crystal display element corresponding to a pixel when the switching element is turned on.

2. The liquid crystal display according to claim 1, wherein the liquid crystal has a relative dielectric constant of not less than 3.

3. The liquid crystal display according to claim 1, further comprising:

a back-light for emitting white light; and

color filters of three primary colors placed between the substrates,

wherein the emitted light is selectively transmitted through the color filters of the three primary colors so as to carry out a color display.

4. The liquid crystal display according to claim 1,
further comprising:

a back-light having light sources for respectively
emitting light rays of three primary colors,

wherein the light sources are allowed to emit light rays
in a time divided manner in synchronism with ON/OFF driving
processes of the switching element so as to carry out a color
display.

5. The liquid crystal display according to claim 1,
wherein the spontaneous polarization of the liquid crystal is a
magnitude of not more than 15 nC/cm^2 .

6. The liquid crystal display according to claim 5,
wherein the liquid crystal has a relative dielectric constant
of not less than 3.

7. The liquid crystal display according to claim 5,
further comprising:

a back-light for emitting white light; and

color filters of three primary colors placed between the
substrates,

wherein the emitted light is selectively transmitted
through the color filters of the three primary colors so as to
carry out a color display.

8. The liquid crystal display according to claim 5,
further comprising:

a back-light having light sources for respectively

emitting light rays of three primary colors,

wherein the light sources are allowed to emit light rays in a time divided manner in synchronism with ON/OFF driving processes of the switching element so as to carry out a color display.

9. The liquid crystal display according to claim 1, wherein the spontaneous polarization of the liquid crystal is a magnitude of not more than 10 nC/cm^2 .

10. The liquid crystal display according to claim 9, wherein the liquid crystal has a relative dielectric constant of not less than 3.

11. The liquid crystal display according to claim 9, further comprising:

a back-light for emitting white light; and

color filters of three primary colors placed between the substrates,

wherein the emitted light is selectively transmitted through the color filters of the three primary colors so as to carry out a color display.

12. The liquid crystal display according to claim 9, further comprising:

a back-light having light sources for respectively emitting light rays of three primary colors,

wherein the light sources are allowed to emit light rays in a time divided manner in synchronism with ON/OFF driving

processes of the switching element so as to carry out a color display.

13. The liquid crystal display according to claim 1, wherein the spontaneous polarization of the liquid crystal is a magnitude of not more than 7 nC/cm^2 .

14. The liquid crystal display according to claim 13, wherein the liquid crystal has a relative dielectric constant of not less than 3.

15. The liquid crystal display according to claim 13, further comprising:

a back-light for emitting white light; and

color filters of three primary colors placed between the substrates,

wherein the emitted light is selectively transmitted through the color filters of the three primary colors so as to carry out a color display.

16. The liquid crystal display according to claim 13, further comprising:

a back-light having light sources for respectively emitting light rays of three primary colors,

wherein the light sources are allowed to emit light rays in a time divided manner in synchronism with ON/OFF driving processes of the switching element so as to carry out a color display.

ABSTRACT OF THE DISCLOSURE

A liquid crystal display in which a liquid crystal, which has a spontaneous polarization of a magnitude of not more than $1/2$ of a maximum quantity of charge that is injected to each pixel when the switching element is turned on, is filled between alignment films formed on respective glass substrates, so as to form a liquid crystal layer thereby to drive the liquid crystal by using a lower applied voltage.

FIG. 1
PRIOR ART

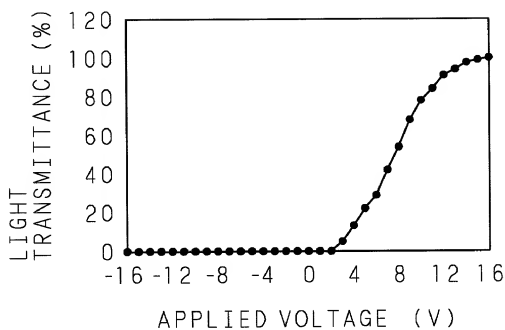


FIG. 2

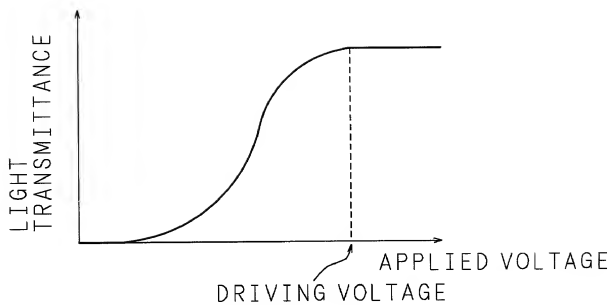


FIG. 3A

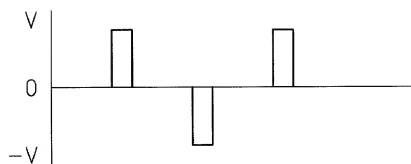


FIG. 3B

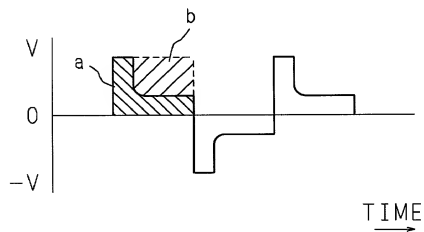
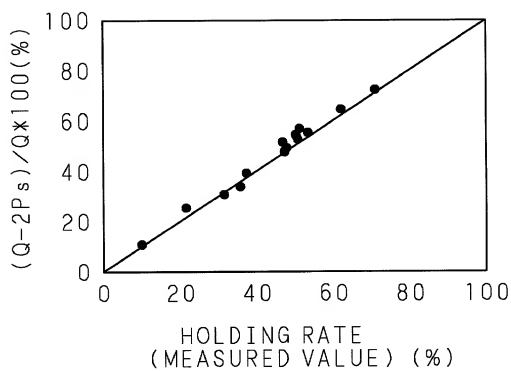
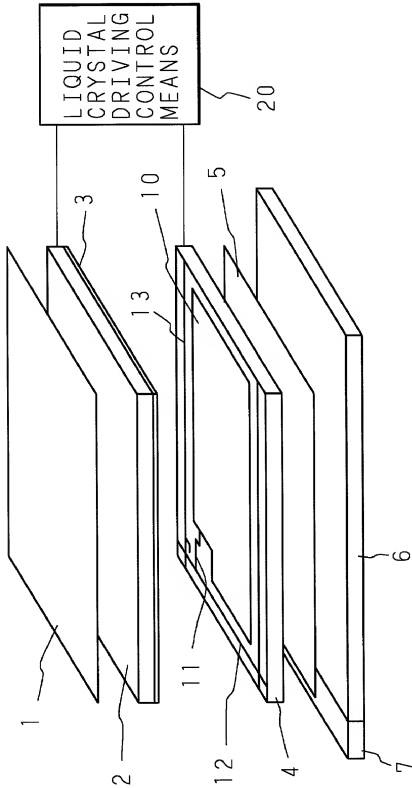


FIG. 4



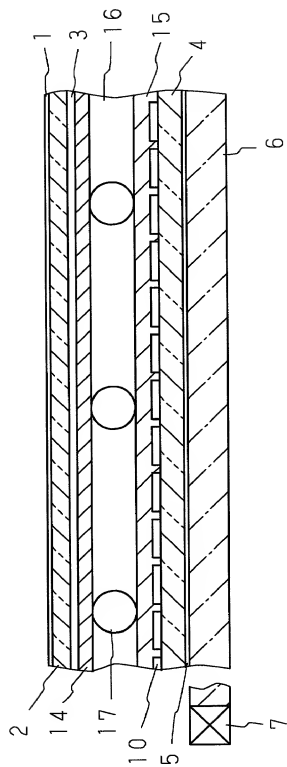
900000 30000000

FIG. 5



- 1: POLARIZING FILM
- 2: GLASS SUBSTRATE
- 3: COMMON ELECTRODE
- 4: GLASS SUBSTRATE
- 5: POLARIZING FILM
- 6: LIGHT-GUIDING AND DIFFUSING PLATE
- 7: LIGHT SOURCE
- 10: PIXEL ELECTRODE
- 11: TFT
- 12: DATA LINE
- 13: SCAN LINE
- 20: LIQUID CRYSTAL DRIVING CONTROL MEANS

FIG. 6



- 14: ALIGNMENT FILM
15: ALIGNMENT FILM
16: LIQUID CRYSTAL LAYER
17: SPACER

FIG. 7

EXAMPLE 1

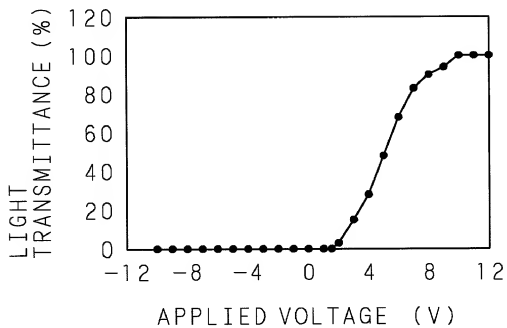


FIG. 8

EXAMPLE 2

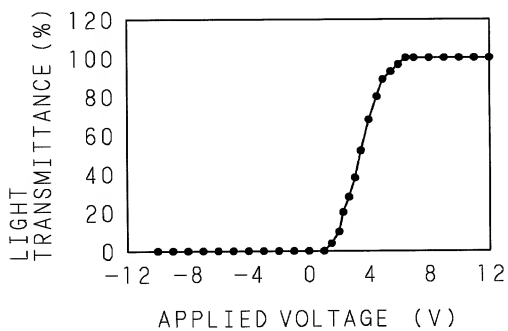


FIG. 9

EXAMPLE 3

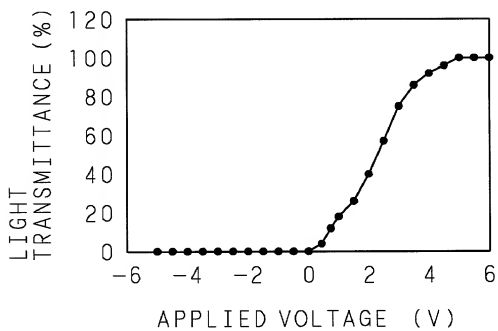


FIG. 10

EXAMPLE 4

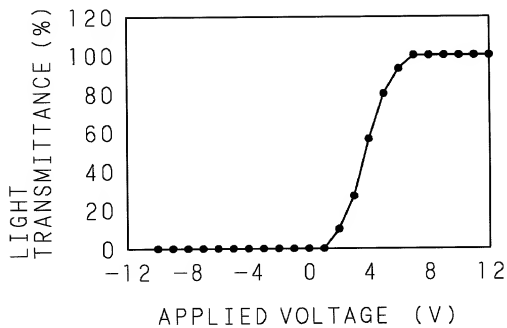
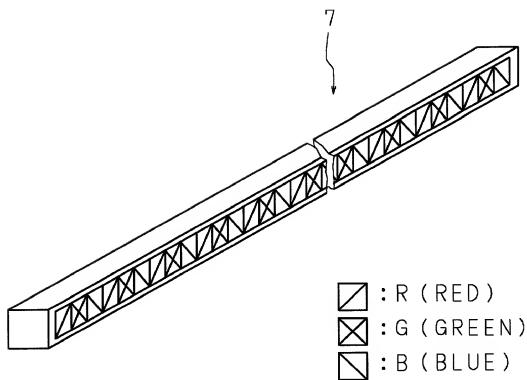


FIG. 11



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Declaration and Power of Attorney For Patent Application

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Japanese Language Declaration

日本語宣言書

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As a below named inventor, I hereby declare that:

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I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

LIQUID CRYSTAL DISPLAY

上記発明の明細書（下記の欄でX印がついていない場合は、本書に添付）は、

the specification of which is attached hereto unless the following box is checked:

☐ 月 日に提出され、米国出願番号または特許協定条約
国際出願番号を _____ とし、
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☐ was filed on _____
as United States Application Number or
PCT International Application Number
_____ and was amended on
_____ (if applicable).

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Prior Foreign Application(s)

外国での先行出願
11-316008

Japan

(Number)

(番号)

(Country)

(国名)

(Number)

(番号)

(Country)

(国名)

I hereby claim foreign priority under Title 35, United States Code, Section 119 (a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 36E(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

Priority Not Claimed

優先権主張なし

5/11/1999

(Day/Month/Year Filed)

(出願年月日)

☐

(Day/Month/Year Filed)

(出願年月日)

☐

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(Filing Date)

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(Application No.)

(出願番号)

(Filing Date)

(出願日)

(Status: Patented, Pending, Abandoned)

(現況: 特許許可済、係属中、放棄済)

(Application No.)

(出願番号)

(Filing Date)

(出願日)

(Status: Patented, Pending, Abandoned)

(現況: 特許許可済、係属中、放棄済)

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Japanese Language Declaration

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国籍		Residence	
郵便の戻元		Citizenship	
		Post Office Address	
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国籍		Residence	
郵便の戻元		Citizenship	
		Post Office Address	

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